

# Michaela Perez

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## Education

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AUG 2017-DEC 2023    **COMPUTER ENGINEERING**    **The University of Texas at Dallas**  
GPA: 3.571    Professional Societies: IEEE, SWE

## Employment

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MAY 2020-AUG 2020    **Engineering Intern**, Ameren  
Worked in the Supervisory Control and Data Acquisition group, establishing links between RTU devices and our system to reflect changes being made in the power grid and verifying the electrical equivalency of the power grid represented across our software platforms.

MAY 2019-AUG 2019    **Undergraduate Research Fellow**, The Center for Power Optimization of Electro-Thermal Systems  
Developed and tested three 3D-printed, metal heat exchangers for electric vehicle batteries using SolidWorks and LabVIEW. Results indicated that additive manufacturing creates rough and porous surfaces that increase the pressure differential across the heat exchanger.

## Relevant Courses

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### ELECTRICAL NETWORK ANALYSIS

Mesh and nodal analysis. Analysis of two-port elements, first and second order circuits in time domain, and steady state sinusoidal analysis using phasor technique. Laboratory experience.

### DIGITAL CIRCUITS

Design and analysis of combinational logic circuits, latches, flip-flops, and datapath components, including state machine minimization.

### SIGNALS AND SYSTEMS

Fundamentals of continuous and discrete-time signal processing. Linear system analysis including convolution, impulse response, Fourier transform, sampling, and z-transform.

### EMBEDDED SYSTEMS

Introduction to micro-controllers, peripheral devices, serial synchronous (SPI, I2C) and asynchronous communication (UART) interfaces. Developed micro-controller programs using ANSI-C.

### ANALOG INTEGRATED CIRCUITS

Analysis and design of on-chip linear amplifiers, including operational, high-frequency, broadband and feedback amplifiers.

## Projects

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### NATALCHEQ

2020-2021

Designed a non-invasive and wireless veterinary birthing monitor to detect the onset of labor in horses and potentially other animals. Delivered a prototype PCB to our sponsor, PonyUp Technologies.

### LADIES IN TECH MENTORING PROGRAM

FALL 2020

A joint program between SWE and Women Who Compute to connect women within STEM Majors at The University of Texas Dallas and help students feel connected to the university, their major, and other women engineers through professional workshops and networking events.

## Skills

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### ENGINEERING

PCB Layout, Schematic Capture, Altium Designer, Fusion 360, LATEX, PSpice, Verilog, Git, Unix Systems

### PROGRAMMING

C++, Python, MIPS Assembly, Verilog, MATLAB

### LABORATORY

Oscilloscope, VNA, DMM, Signal Generator, Soldering, Power Supply

### PROFESSIONAL

Agile, Calendar Management, Microsoft Office, Outlook 365, MS Teams, MS Excel